

NOV 03 2005

Attorney Docket No: YOR920030605US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re PATENT APPLICATION of:

Robert L. FRANCH et al.

Appln. No: 10/824,297

Art Unit: 2859

Filed: APRIL 14, 2004

Examiner: Gail VERBITSKY

For: ON CHIP TEMPERATURE MEASURING
AND MONITORING CIRCUIT
AND METHOD**RESPONSE TO RESTRICTION REQUIREMENT**Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir,

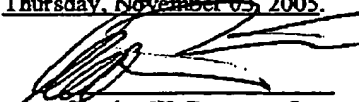
Responsive to a Restriction Requirement dated October 5, 2005, Applicants hereby elect the integrated circuit of Group I, as represented in Claims 1 to 11 without traverse.

Should the Examiner believe anything further may be required, the Examiner is requested to contact the Applicants' undersigned attorney at the local telephone number listed below for a telephonic or personal interview to discuss any other changes.

Respectfully Submitted,

November 3, 2005
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I hereby certify that this document is being transmitted by fax to the Commissioner for Patents, United States Patent and Trademark Office, Washington, D.C. 20231, at fax no. (571) 273-8300, this Thursday, November 03, 2005.


Charles W. Peterson, Jr.
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